

DELAY LOCKED LOOP SYNTHESIZER WITH MULTIPLE OUTPUTS
AND DIGITAL MODULATION

ABSTRACT OF THE DISCLOSURE

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A delay locked loop circuit (200) in which multiple outputs are produced. A single delay line (24) is shared among multiple tap selection circuits (256A, 265B, 265C). Fixed phase shifts (412) can be introduced between multiple outputs. A modulating signal can be used in the tap selection processing to produce digital amplitude, frequency
10 and/or phase modulation.

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